AMENDMENTS TO CLAIMS

Kindly amend claims 1 and 7 as follows.

1.   (Currently Amended) A system for automatically updating the revision level of programmable devices comprising:

a master programmable device having pulse receiving logic and a memory space operatively disposed therein, wherein said master programmable device is a one of group consisting of a field programmable gate array and a n erasable programmable logic device; and

at least one slave programmable device having pulse generating logic operatively disposed therein, said slave programmable device coupled to said master programmable device through an interface and configured to send revision information to said master programmable device responsive to generating said revision information in response to receiving a reset signal wherein said at least one slave programmable device is a one of group consisting of a field programmable gate array and an erasable programmable logic device.

2.   (Original) The system of claim 1, wherein said memory space further includes a revision register containing one or more memory locations, each said memory location corresponding to a slave programmable device.

3.   (Original) The system of claim 1, wherein said revision information comprises a pulse stream corresponding to the revision level of a slave programmable device.

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4. (Original) A system for collecting programmable device revision information comprising:

means for sending, by said system, a reset signal to a master programmable device and at least one slave programmable device thereby placing all programmable devices in a known good condition;

means for sending, by said at least one slave programmable device, revision information to said master programmable device;

means for receiving, by said master programmable device, said revision information; and

means for storing, by said master programmable device, said revision information.

5. (Original) The system of claim 4, wherein said means for storing comprises a revision register containing one or more memory locations, each said memory location corresponding to a slave programmable device.

6. (Original) The system of claim 4, wherein said revision information comprises a pulse stream corresponding to the revision level of a slave programmable device.

7. (Currently Amended) A method for updating programmable device revision information in a system having a master programmable device wherein said master programmable device is a one of group consisting of a field programmable gate array and an erasable programmable logic device and at least one slave programmable device wherein said at least one slave programmable device is a one of group consisting

of a field programmable gate array and an erasable programmable logic device, comprising:

resetting all devices in said system;

receiving a reset signal in said at least one slave programmable device;

generating revision information in said at least one slave programmable device responsive to receiving said reset signal;

sending revisions said revision information from said at least one slave programmable device to said master programmable device; and

storing said revisions revision information on said master programmable device.